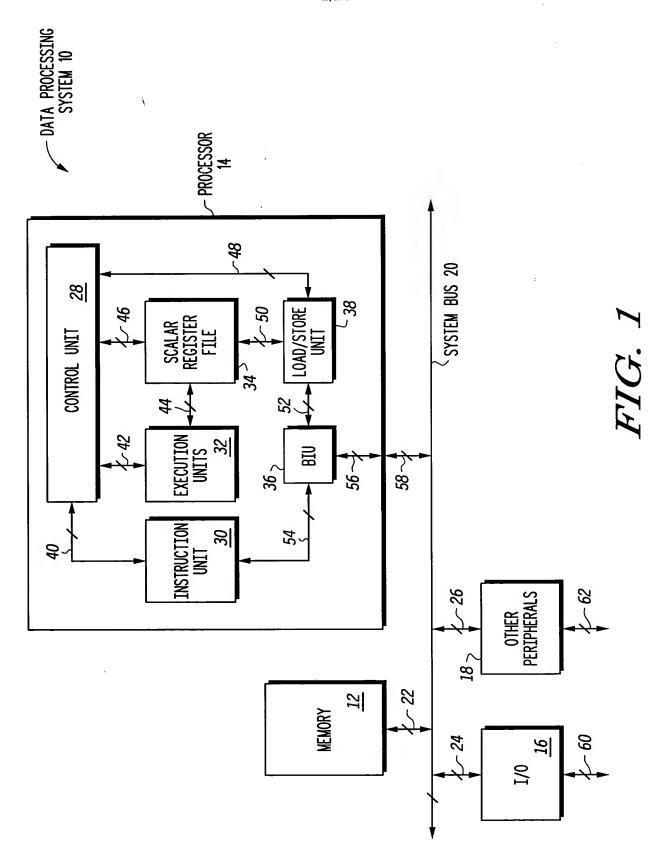
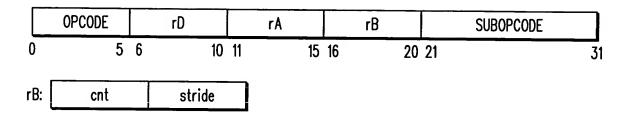


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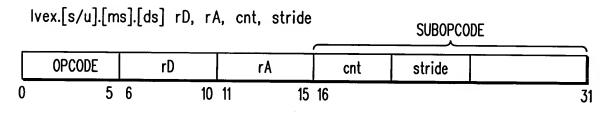


lvex.[s/u].[ms].[ds] rD, rA, rB

Γ



#### FIG. 2



# FIG. 3

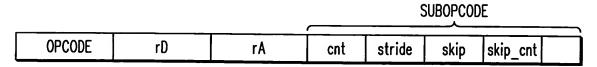
Imvex.[s/u].[ms].[ds] rD, rA, rB

OPCODE	rD	rA	rB	SUBOPCODE

rB:	cnt	stride	skip	skip_cnt
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#### FIG. 4

Imvex.[s/u].[ms].[ds] rD, rA, cnt, stride, skip, skip\_cnt



Imvex2.[s/u].[ms].[ds] rD, rA, rB

 $\Gamma$ 

OPCODE rD rA	rB	SUBOPCODE
--------------	----	-----------

rB: cnt rcnt stride skip

### FIG. 6

 $lstrmvex.[s/u].[ms].[ds] \ rD, \ rA, \ rB$ 

OPCODE	rD	rA	rB	SUBOPCODE

rB: cnt rcnt stride skip skip\_cnt

# FIG. 7

 $stvex.[s/u].[ms].[ss].[h/I] \ rS, \ rA, \ rB$ 

OPCODE	rS	rA	rB	SUBOPCODE

rB: cnt stride

 $stmvex.[s/u].[ms].[ss].[h/l] \ rS, \ rA, \ rB$ 

 $\Gamma$ 

	OPCODE	rS	rA	rB	SUBOPCODE
--	--------	----	----	----	-----------

rB: cnt stride skip skip\_cnt

# FIG. 9

stmvex2.[s/u].[ms].[ss].[h/I] rS, rA, rB

OPCODE	rS	rA	rB	SUBOPCODE

rB: cnt rcnt stride skip

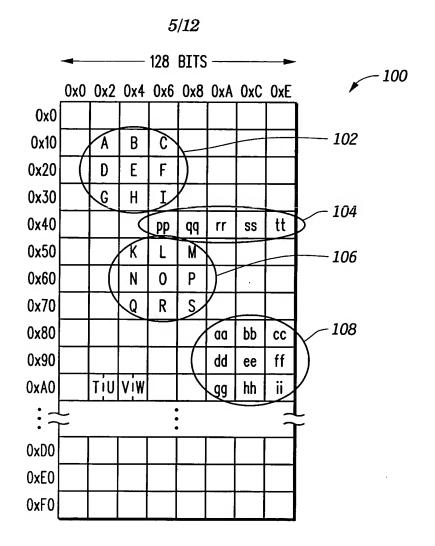
# FIG. 10

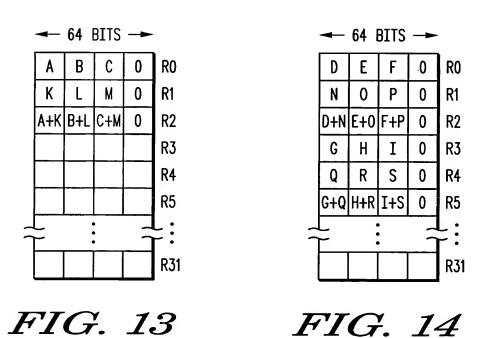
ststrmvex.[s/u].[ms].[ss].[h/l] rS, rA, rB

000005	•	A		OUDODOODE
OPCODE	rS	rA	rB	SUBOPCODE

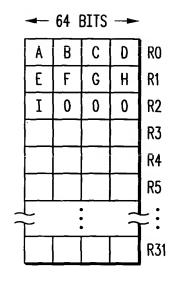
rB: cnt rcnt stride skip skip\_cnt

Г





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Г

	← 64 BITS ←							
	A	В	С	0	RO			
	D	E	F	0	R1			
	G	Н	I	0	R2			
					R3			
	<b>-</b> iT	-iU	<b>-</b> iV	0	R4			
					R5			
(				7				
					R31			

FIG. 16

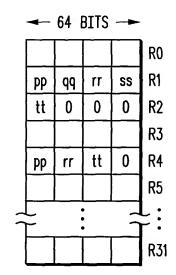


FIG. 17

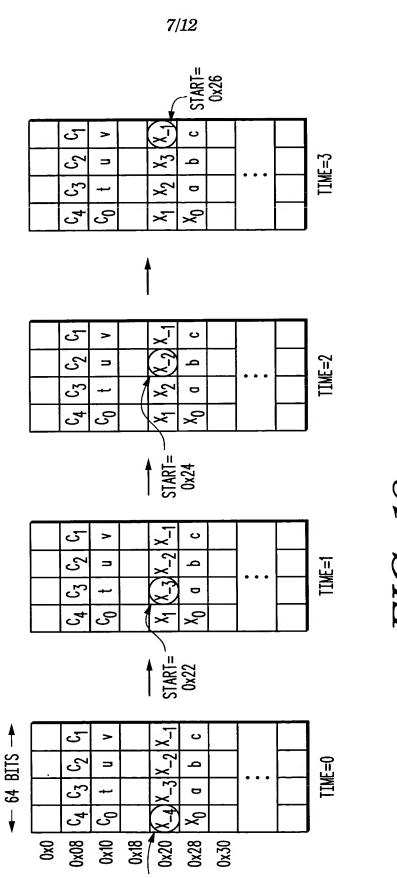


FIG. 18

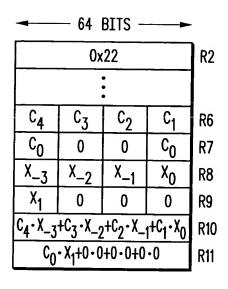
← 64 BITS ← ►						
	0x08					
	0x	20		R2		
	:					
C <sub>4</sub>	$C_4$ $C_3$ $C_2$ $C_1$					
	0	0	$c_0$	R7		
X <sub>-4</sub>	X <sub>-3</sub>	X <sub>-2</sub>	X <sub>-1</sub>	R8		
$X_0$	$X_0  0  0  0$					
$C_4 \cdot X_{-4}$	C <sub>4</sub> • X <sub>-4</sub> +C <sub>3</sub> • X <sub>-3</sub> +C <sub>2</sub> • X <sub>-2</sub> +C <sub>1</sub> • X <sub>-1</sub>					
$c_0$	·X <sub>0</sub> +0·0	)+0•0+0	•0	R11		

Г

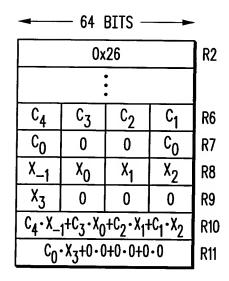
# FIG. 19

64 BITS						
	0x24					
	:					
C <sub>4</sub>	$c_4$ $c_3$ $c_2$ $c_1$					
c <sup>0</sup>	0	0	c <sub>0</sub>	R7		
X <sub>-2</sub>	$X_{-2}$ $X_{-1}$ $X_0$ $X_1$					
$x_2$	$X_2 \mid 0 \mid 0 \mid 0$					
$C_4 \cdot X_{-2}$	R10					
$c_0$	·X <sub>2</sub> +0·0	)+0-0+0	-0	R11		

FIG. 21



# FIG. 20



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← 64 BITS ←						
A B C O						
K	N	Q	0	R2		
0x12						
0x54						

# FIG. 23

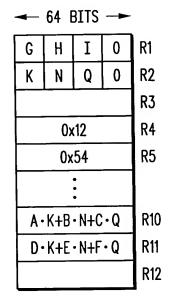


FIG. 25

← 64 BITS ←							
R1							
R2							
R3							
R4							
R5							
R10							
R11							
R12							
)							

# FIG. 24

<b>—</b>	64 (	BITS	-						
G	G H I O								
L	0	R	0	R2					
	0x12								
	0x54								
Α-	R10								
D.	R11								
G.	K+H	·N+I	·Q	R12					

Imvex\_skip\_once.[s/u].[ms].[ds] rD, rA, rB

ODCODE	0		r — — — — — — — — — — — — — — — — — — —	
OPCODE	ן רט	rA	rB	SUBOPCODE
				0000,0000

rB: cnt stride skip skip\_cnt

FIG. 27

Imvex\_cb.[s/u].[ms].[ds] rD, rA, rB

	OPCODE	rD	rA	rB	SUBOPCODE
--	--------	----	----	----	-----------

rB: buffer\_size offset

Γ

# FIG. 28

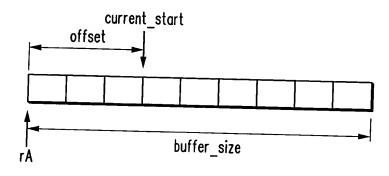


FIG. 29

lstrmvex\_cb.[s/u].[ms].[ds] rD, rA, rB

TD rA rB SUBOPCODE	OPC	CODE	rD	rA	rB	SUBOPCODE
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rB: buffer\_size offset

 $Imvex_fft.[s/u].[ms].[ds] rD, rA, rB$ 

OPCODE !	rn i	rA	rD	CHDODOODE
01 0002		17	ID	SUBOPCODE

rB: radix

 $\Gamma$ 

FIG. 31

 $stmvex\_fft.[s/u].[ms].[ss] \ rS, \ rA, \ rB$ 

	OPCODE	rD	rA	rB	SUBOPCODE
•					

rB: radix

FIG. 32

lmstrmvex\_fft.[s/u].[ms].[ds] rD, rA, rB

Г			<del> </del>		
	OPCODE	rŊ	r A	rD	CLIDADCADE
L	OT OODL	10	١٨	סו	SUBOPCODE
_					L

rB: radix

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	0x0							0xE	300
0x0									
0x10				Х <sub>0</sub>	X <sub>1</sub>	X <sub>2</sub>	X3	X <sub>4</sub>	
0x20	X <sub>5</sub>	Х <sub>6</sub>	X <sub>7</sub>						
0x30									
0x40			Y <sub>0</sub>	Y <sub>4</sub>	Y <sub>6</sub>	Y <sub>2</sub>	Υ <sub>1</sub>	Y <sub>5</sub>	
0x50	Y3	Y <sub>7</sub>							
0x60									

FIG. 34

X <sub>0</sub>	X <sub>4</sub>	Х <sub>6</sub>	X <sub>2</sub>	R1
X <sub>1</sub>	X <sub>5</sub>	X <sub>3</sub>	x <sub>7</sub>	R2
				R3
Y <sub>0</sub>	Y <sub>1</sub>	Y <sub>2</sub>	Y <sub>3</sub>	R4
Y <sub>4</sub>	Y <sub>5</sub>	Y <sub>6</sub>	Y <sub>7</sub>	R5

FIG. 35